Prior Art Cearch 10/621,330

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|------------|-------|---|---|---------------------|---------|------------------|
| S 5 | 7 | sram-type near fpga | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:56 |
| S6 | 24759 | fpga or (field adj programmable adj gate adj array) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:57 |
| S7 | 150 | S6 with (nonvolatile near memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:58 |
| S8 | 423 | S6 with (non-volatile near memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:58 |
| S9 | 565 | S7 or S8 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:58 |
| S10 | 571 | S6 with (flash near memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:58 |
| S11 | 1030 | S9 or S10 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR . | ON | 2005/08/25 09:58 |
| S12 | 6 | S11 with (configuration near circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 08:59 |
| S13 | 52 | S11 and (configuration near circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:25 |
| S14 | 52 | S12 or S13 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:00 |

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|-----|----------------|---|---|----|----------|------------------|
| S15 | 15 | S14 and processor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:01 |
| S16 | 4 8 | S14 and process\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:24 |
| S17 | 565 | S6 and S9 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:24 |
| S18 | 571 | S6 and S10 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:24 |
| S19 | 1030 | S17 or S18 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:25 |
| S20 | 52 | S19 and (configuration near circuit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:26 |
| S21 | 811 | S19 and configuration | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:28 |
| S22 | 592 | S21 and processor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:27 |
| S23 | 55 | S19 and (configuration near circuit\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:28 |
| S24 | 18 | S23 and processor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:31 |
| S25 | 55 | S11 and (configuration near circuit\$3) /25/2005 2:11:56 PM Page 2 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:33 |

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| S26 | 672 | S11 and processor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:33 |
|-----|-------|--|---|----|-----|------------------|
| 527 | 188 | S11 and (central adj processing adj unit) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:34 |
| S28 | 7 | S27 and (power-on near reset) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:37 |
| S29 | 145 | S27 and controller | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:37 |
| 530 | 22 | S11 and (power-on near reset) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 09:58 |
| S31 | 12716 | ((365/189.01,189.02,189.03,189.05,189.08,189.12,226) or (326/37-41)).CCLS. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/08/25 10:33 |
| 532 | 212 | S31 and (power-on near reset) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:34 |
| S33 | 0 | S31 and (power near on near reset) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:34 |
| S34 | 0 | S31 and (power adj on) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:35 |
| S35 | 0 | S31 and (power adj on) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:35 |
| S36 | 415 | S31 and power-on | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:35 |

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| S37 | 99 | S36 and (non-volatile near memory) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:37 |
|-----|----|------------------------------------|---|----|----|------------------|
| 538 | 87 | S37 and process\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:38 |
| S39 | 14 | S38 and S6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/08/25 10:39 |